



App No.: 10/733,474

Docket No.: M4065.0741/P741

Inventor: Kit Fai Chan et al.

Title: CURRENT MODE LOGIC SCHEME AND CIRCUIT FOR
MATCH LINE SENSE AMPLIFIER DESIGN USING
CONSTANT CURRENT BIAS CASCODE CURRENT
MIRRORS

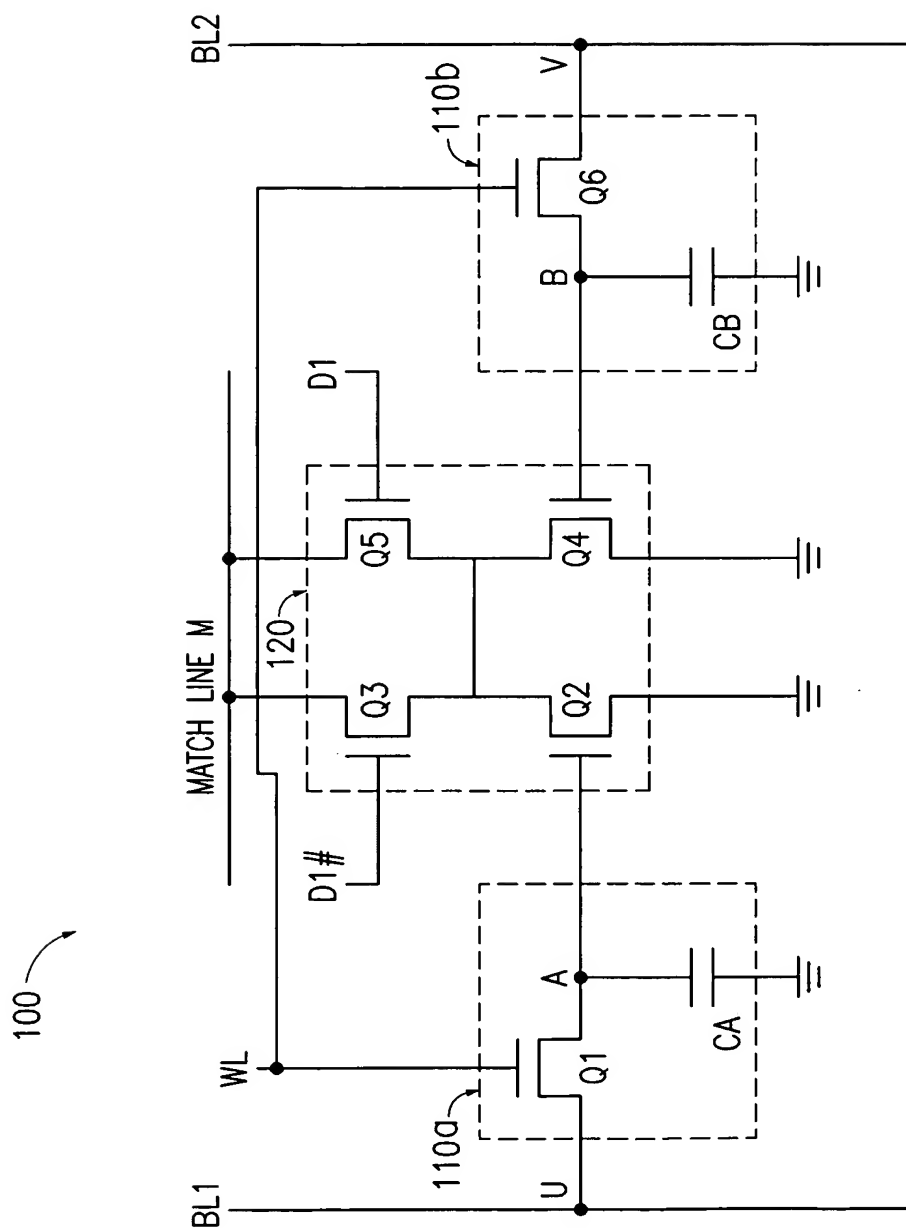


FIG. 1 (PRIOR ART)



FIG. 2 (PRIOR ART)

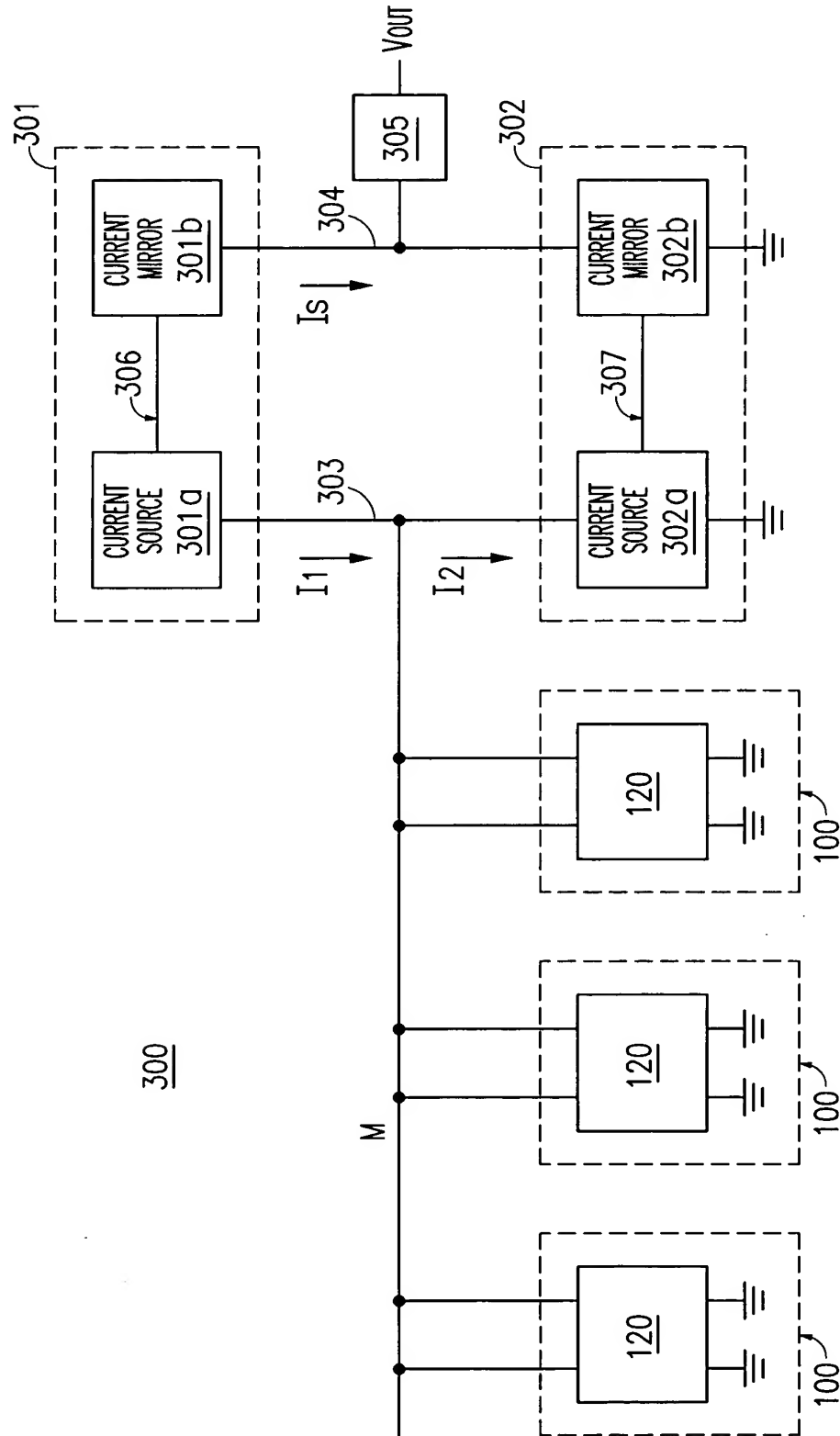


FIG. 3

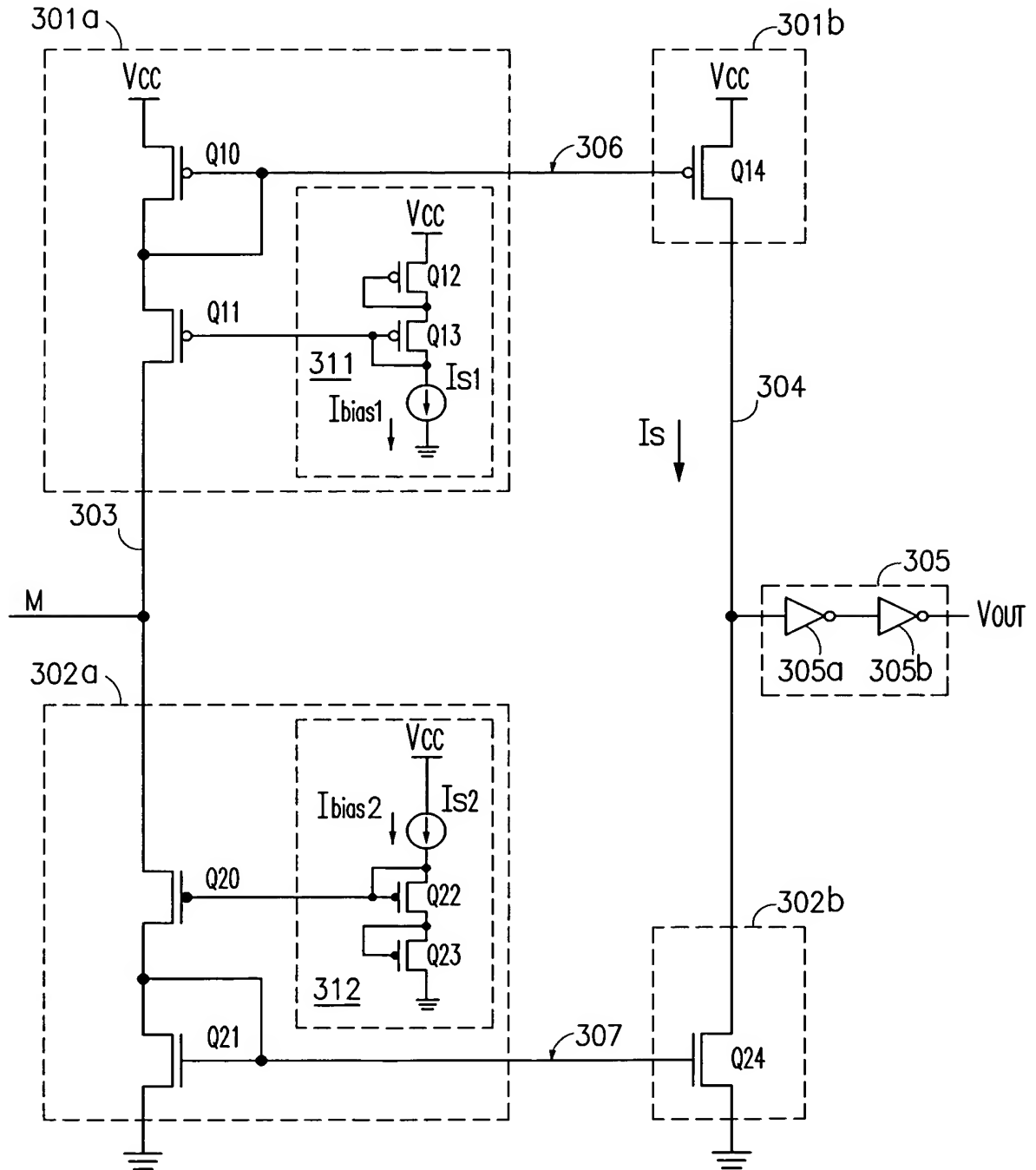


FIG. 4

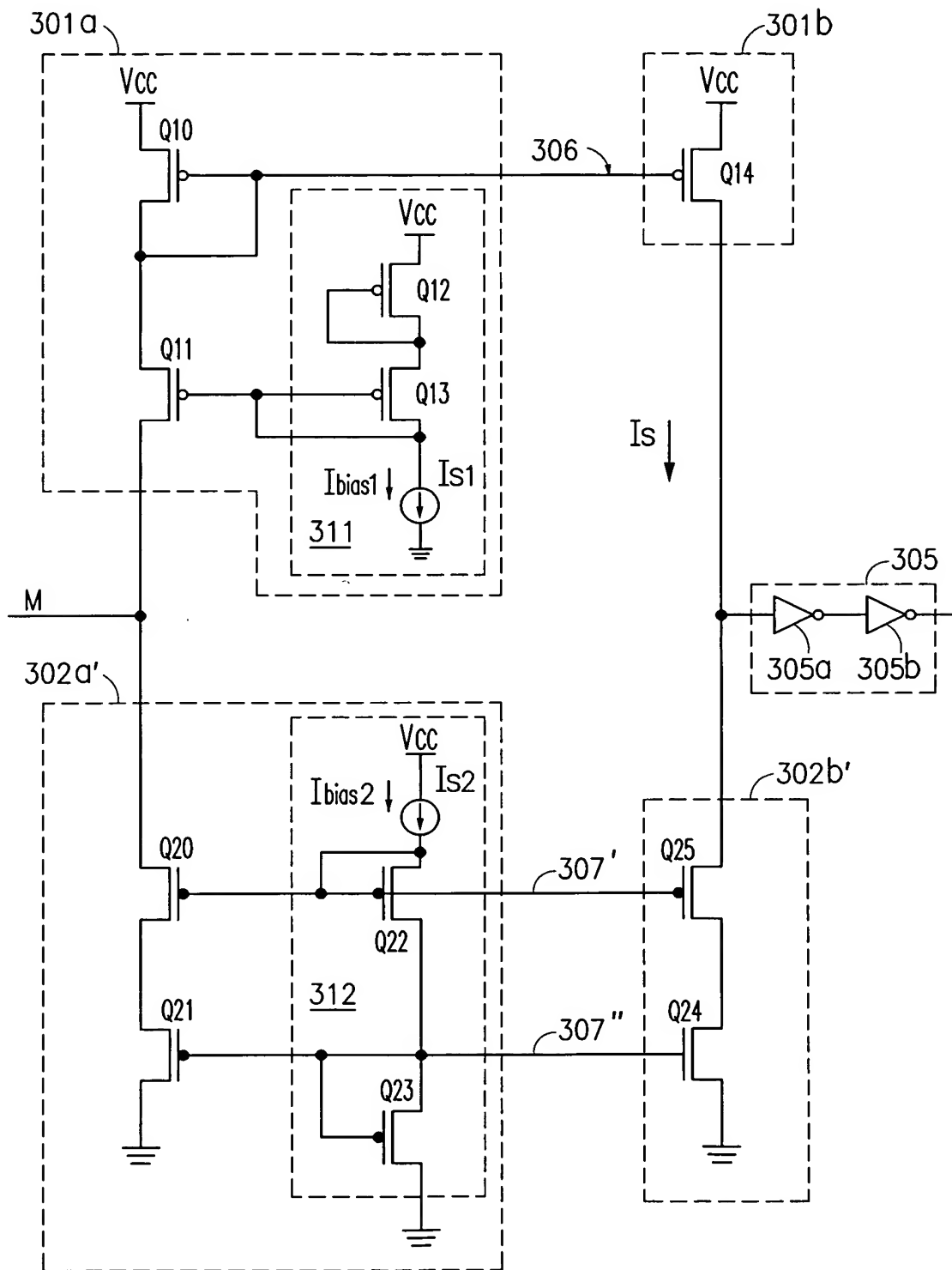


FIG. 5

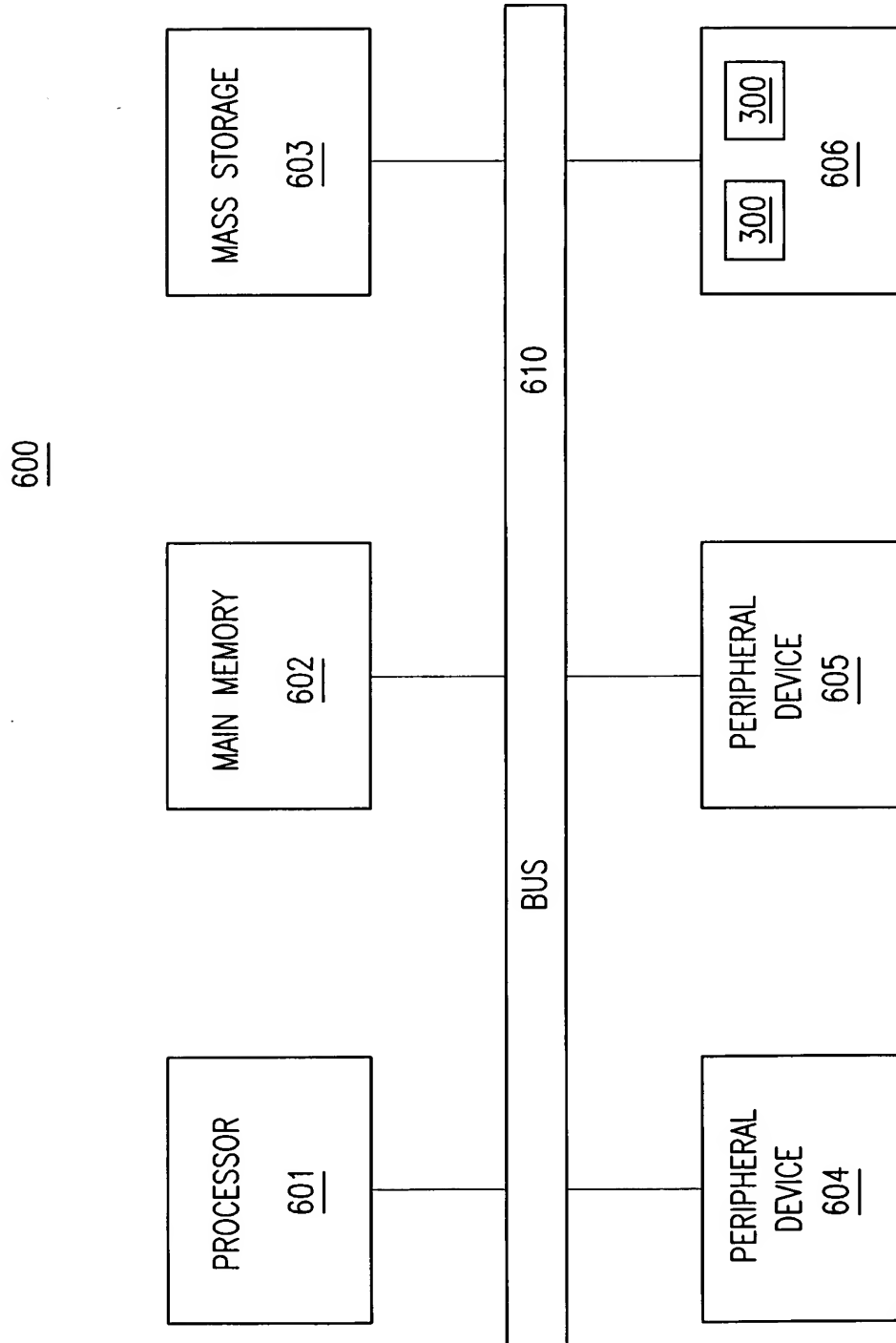


FIG. 6